

Fig.1

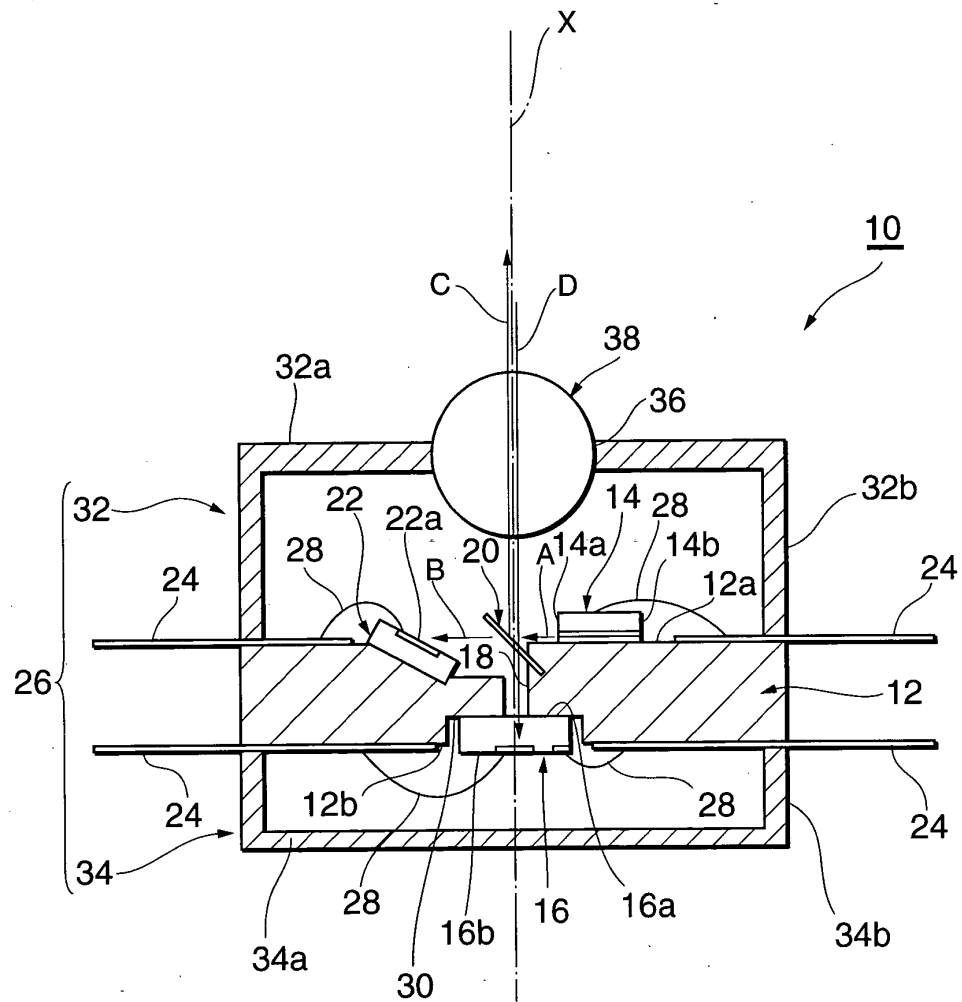


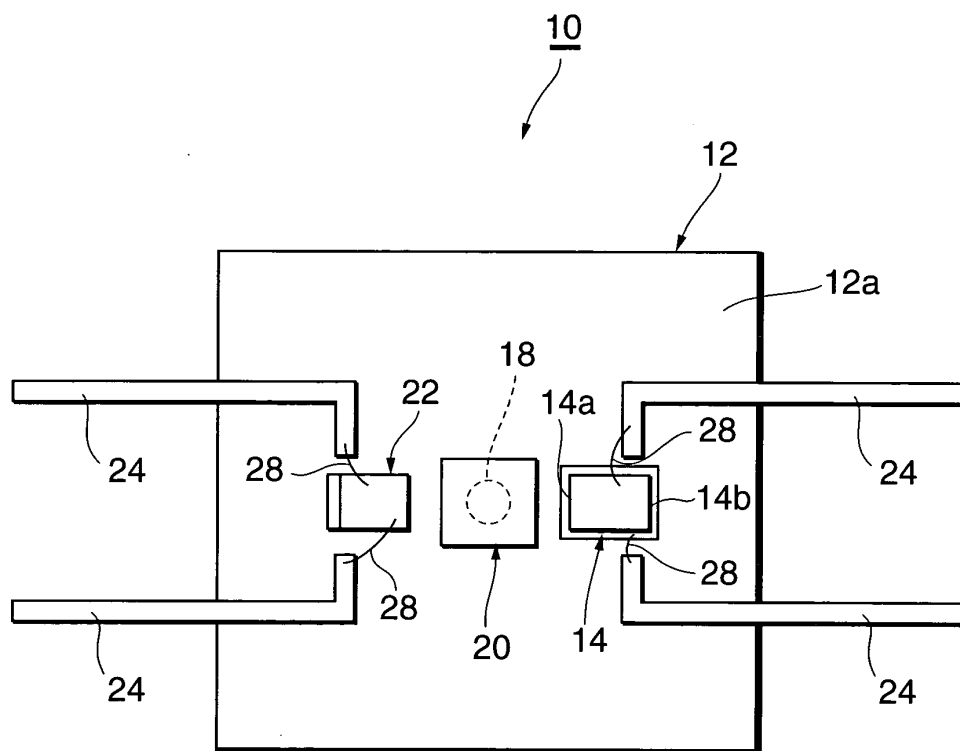
Fig.2

Fig.3

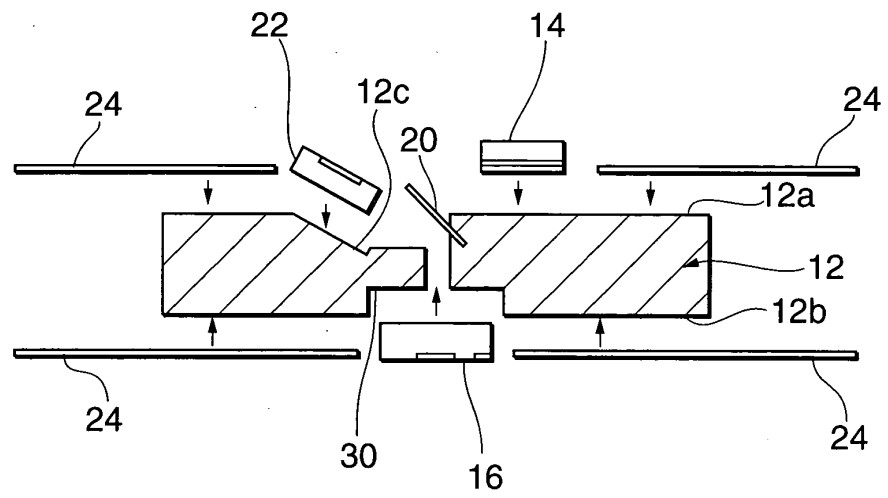


Fig.5

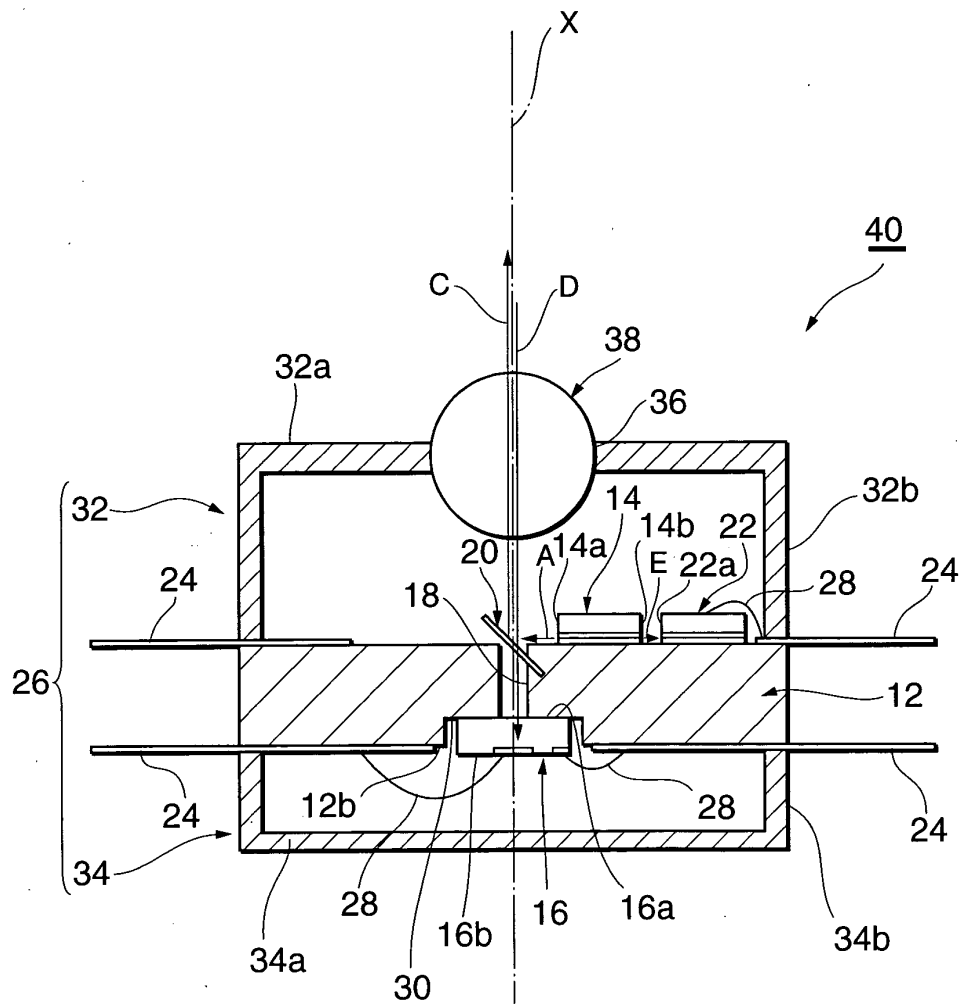


Fig.7

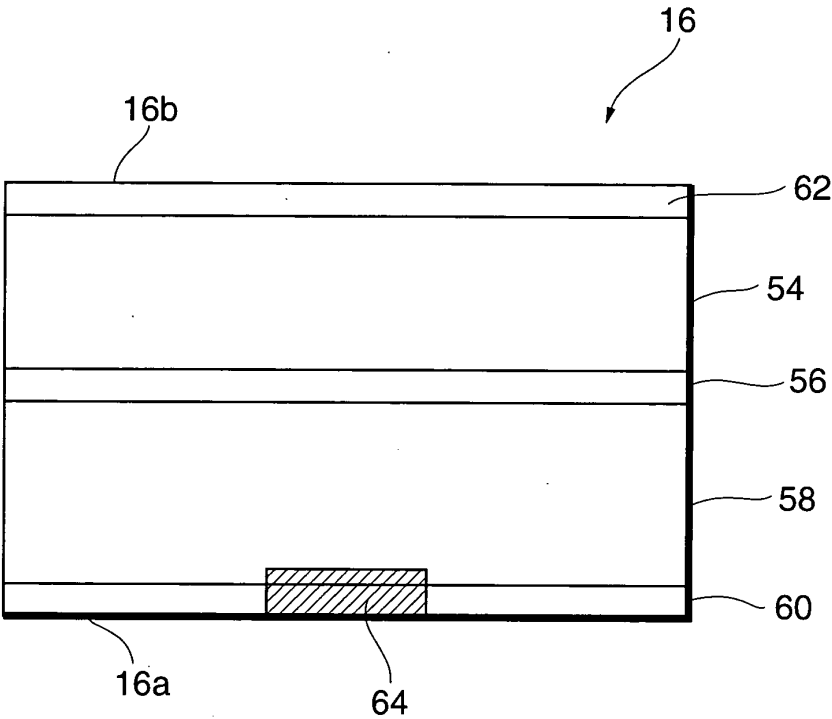
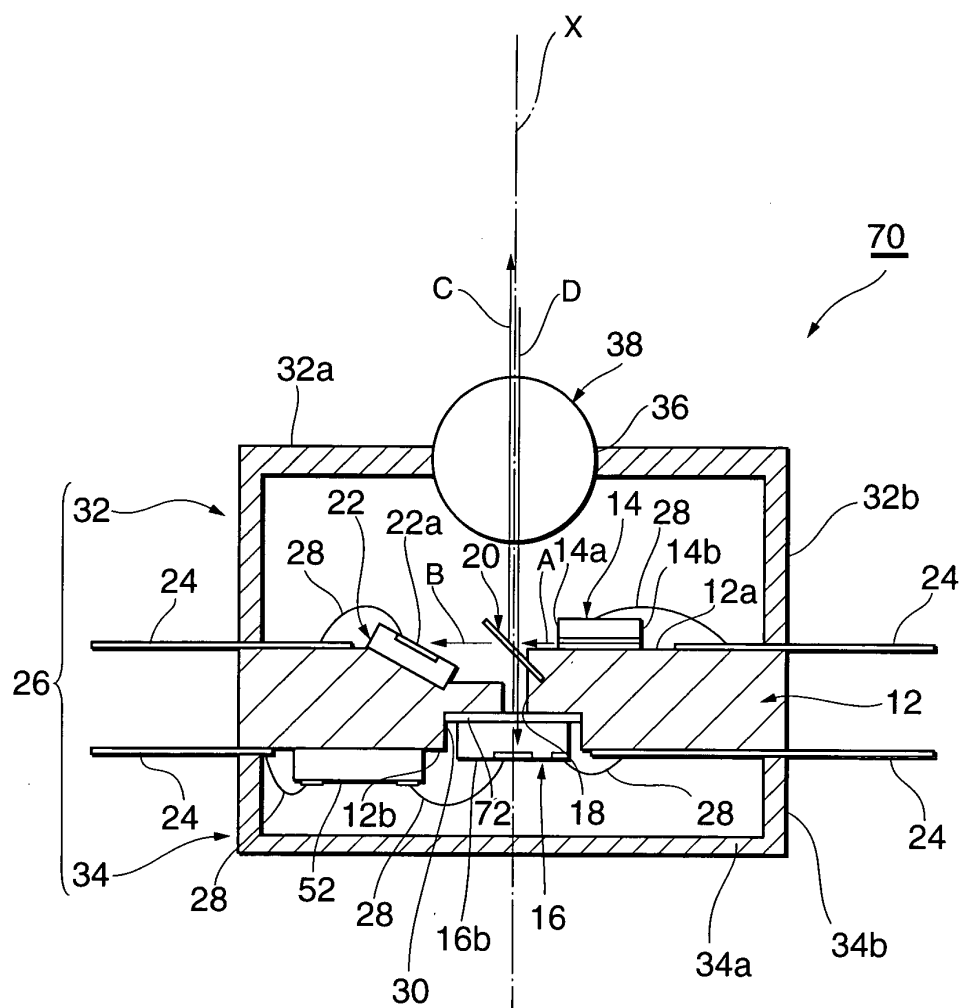


Fig.8

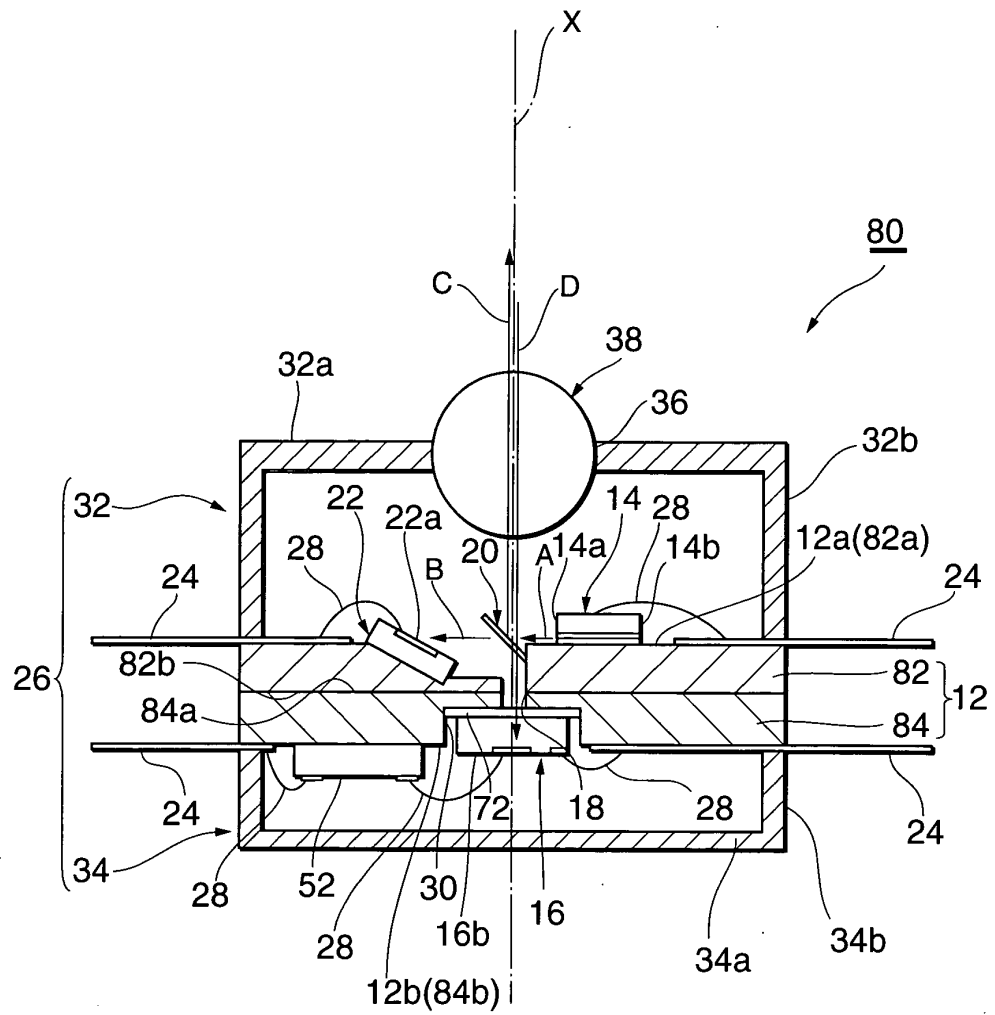


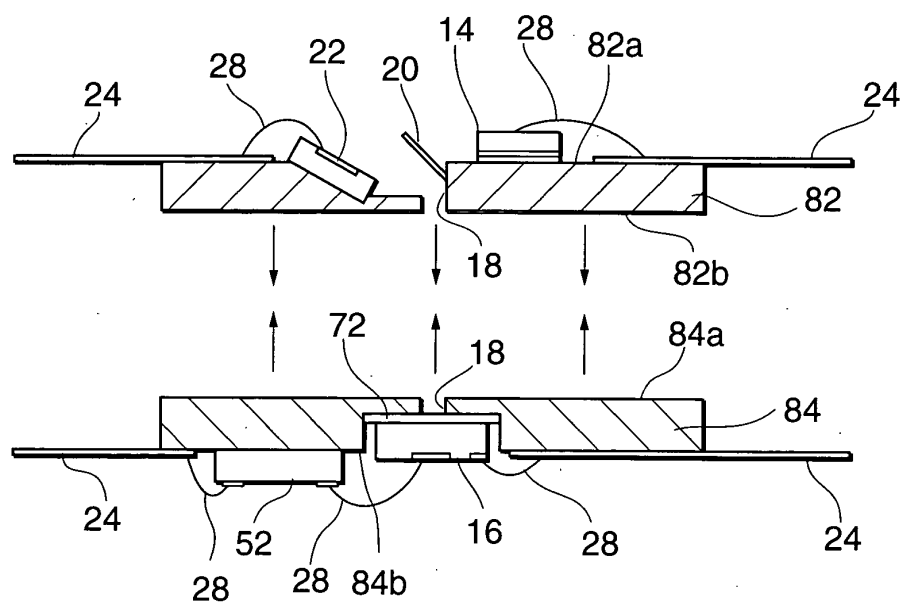
Fig.10

Fig.11

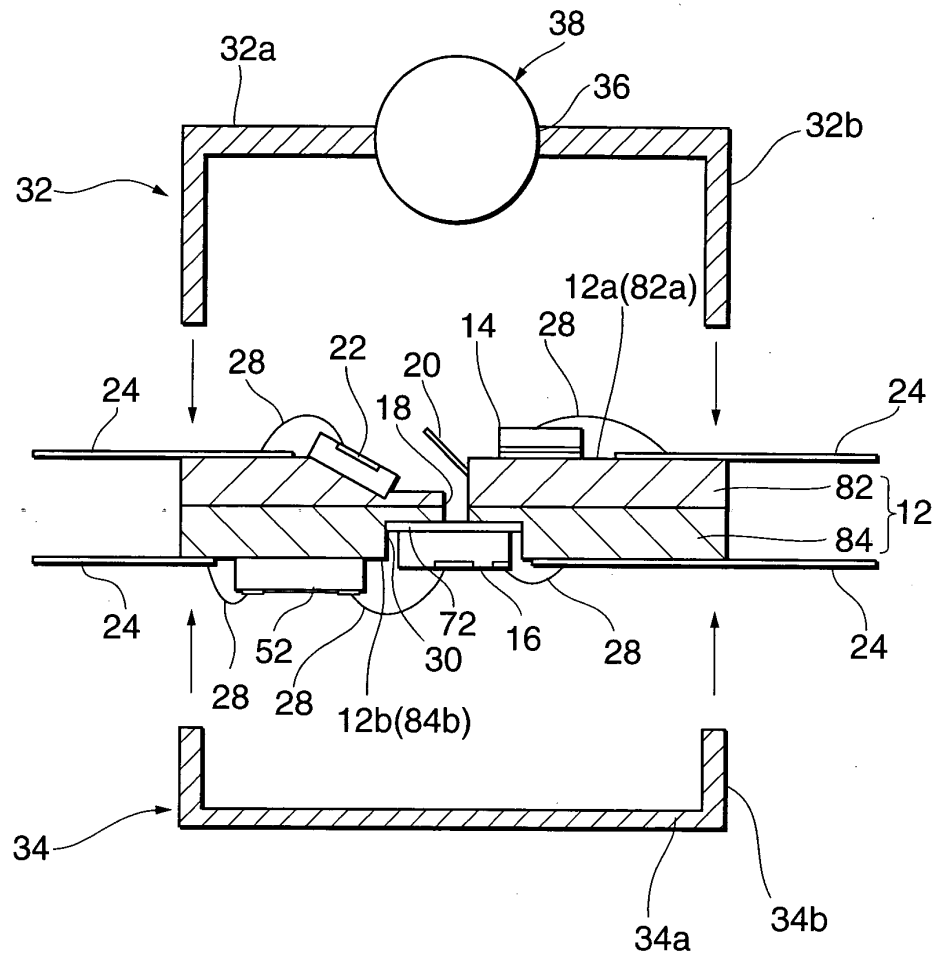
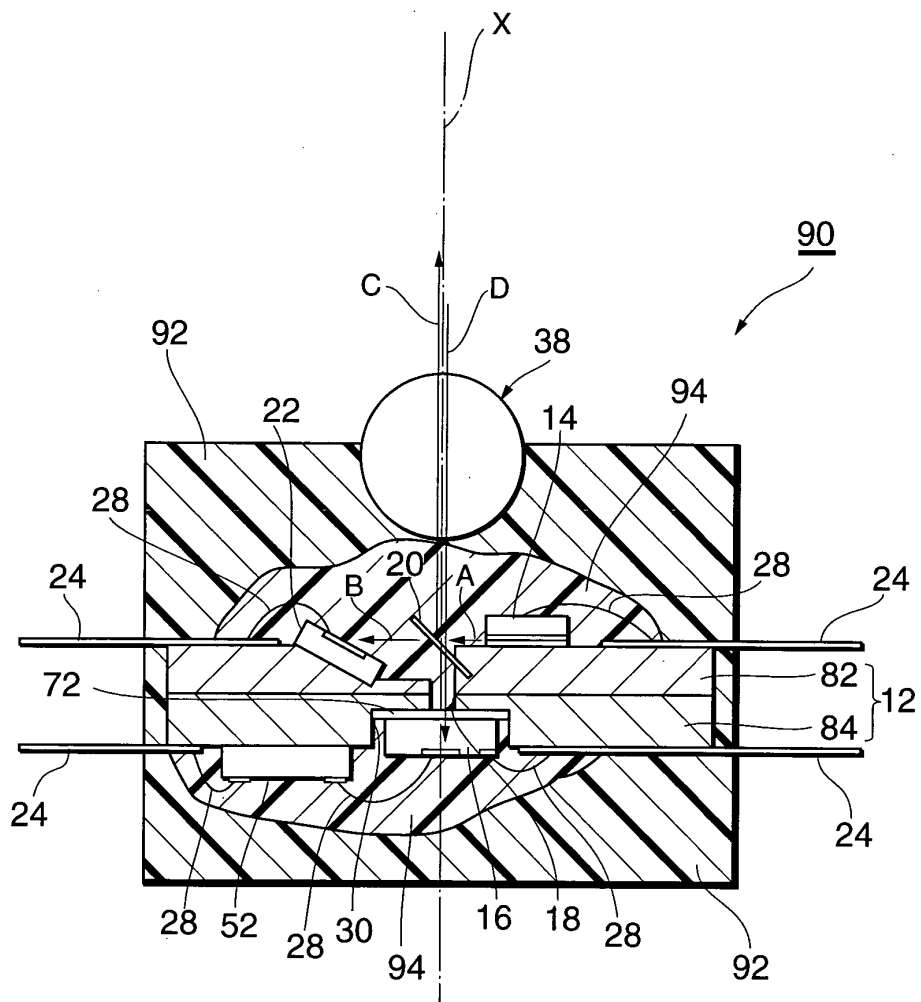


Fig.12



This cross-sectional view shows a semiconductor device assembly. A substrate 12 is shown with a top layer 24 and a bottom layer 24. A central region 16 is defined by a top layer 94 and a bottom layer 94. A central region 20 is defined by a top layer 28 and a bottom layer 28. A central region 22 is defined by a top layer 94 and a bottom layer 94. A central region 24 is defined by a top layer 24 and a bottom layer 24. A central region 28 is defined by a top layer 28 and a bottom layer 28. A central region 38 is defined by a top layer 38 and a bottom layer 38. A central region 96a is defined by a top layer 96a and a bottom layer 96a. A central region 96b is defined by a top layer 96b and a bottom layer 96b. A central region 96c is defined by a top layer 96c and a bottom layer 96c. A central region 96 is defined by a top layer 96 and a bottom layer 96. A central region 96a is defined by a top layer 96a and a bottom layer 96a. A central region 96b is defined by a top layer 96b and a bottom layer 96b. A central region 96c is defined by a top layer 96c and a bottom layer 96c. A central region 96 is defined by a top layer 96 and a bottom layer 96.

Fig.15

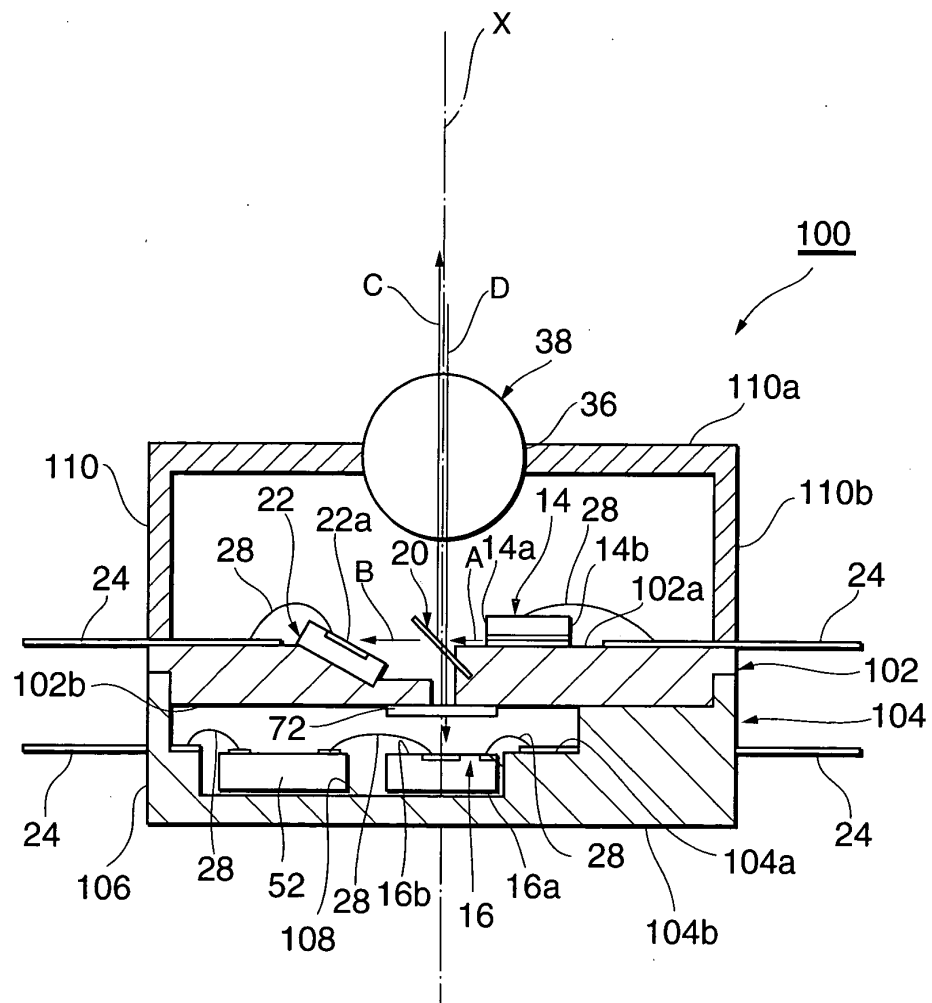


Fig.16

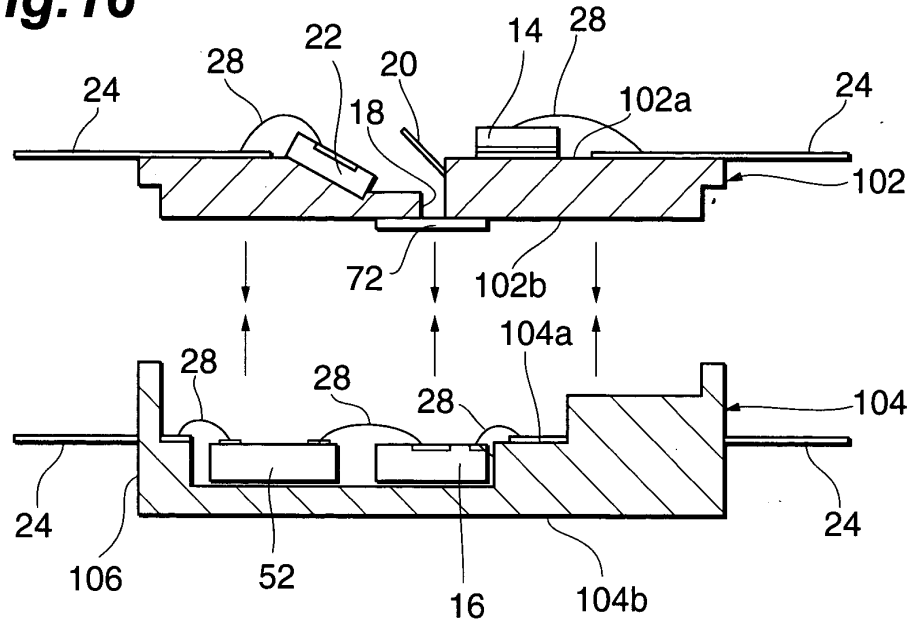


Fig.17

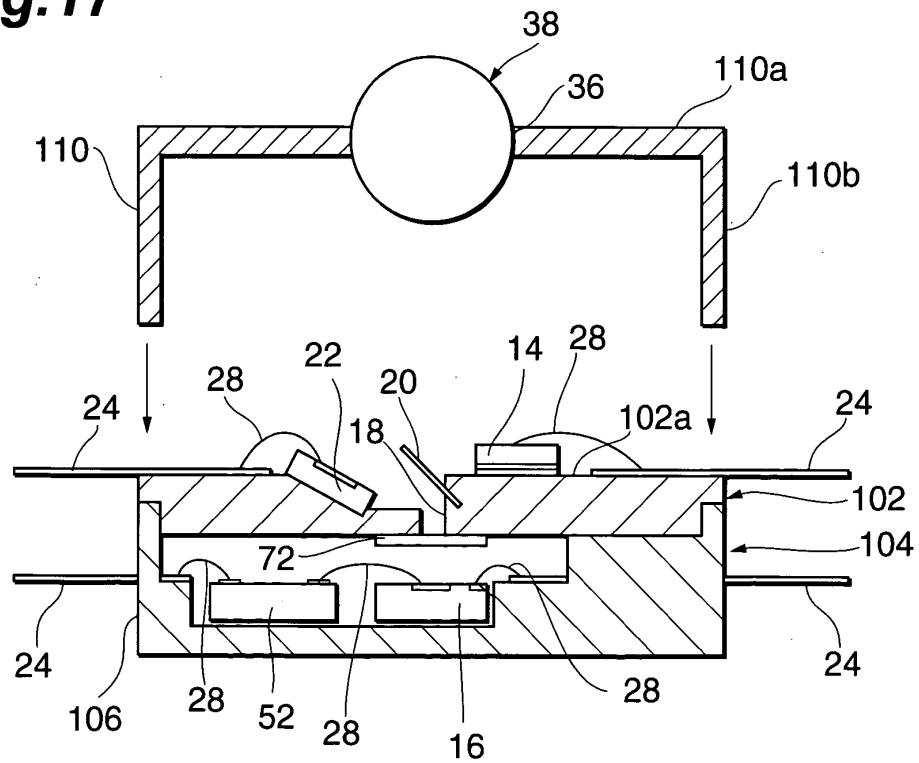


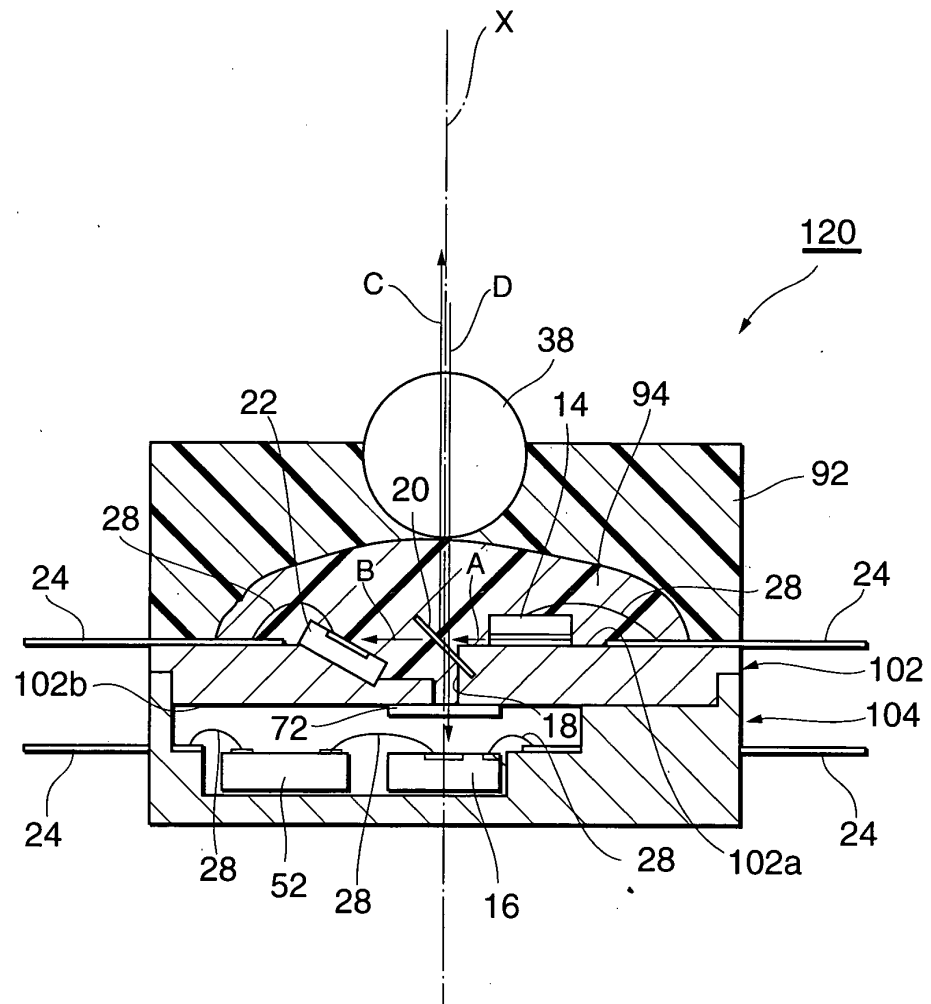
Fig.18

Fig.19

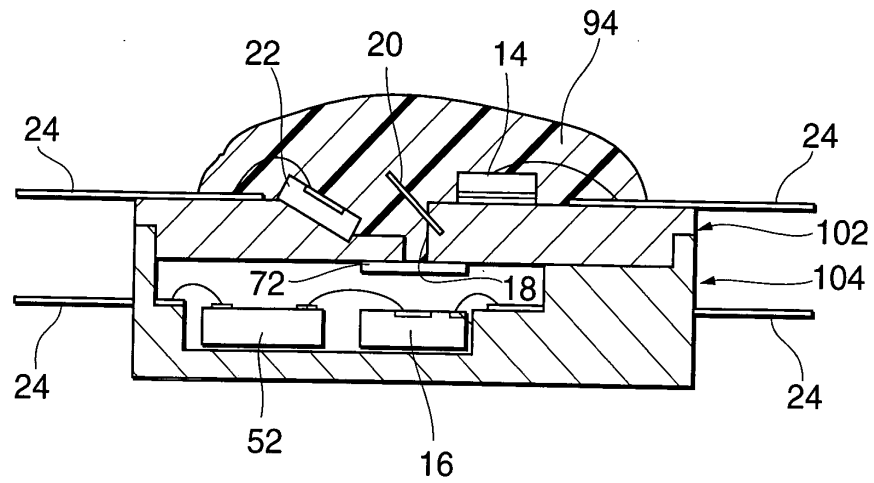


Fig.20

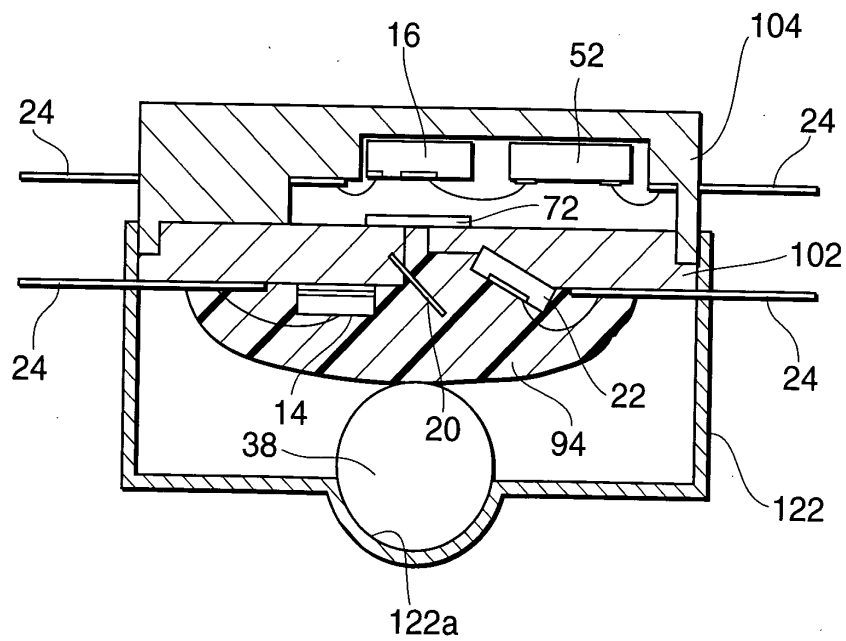


Fig.21

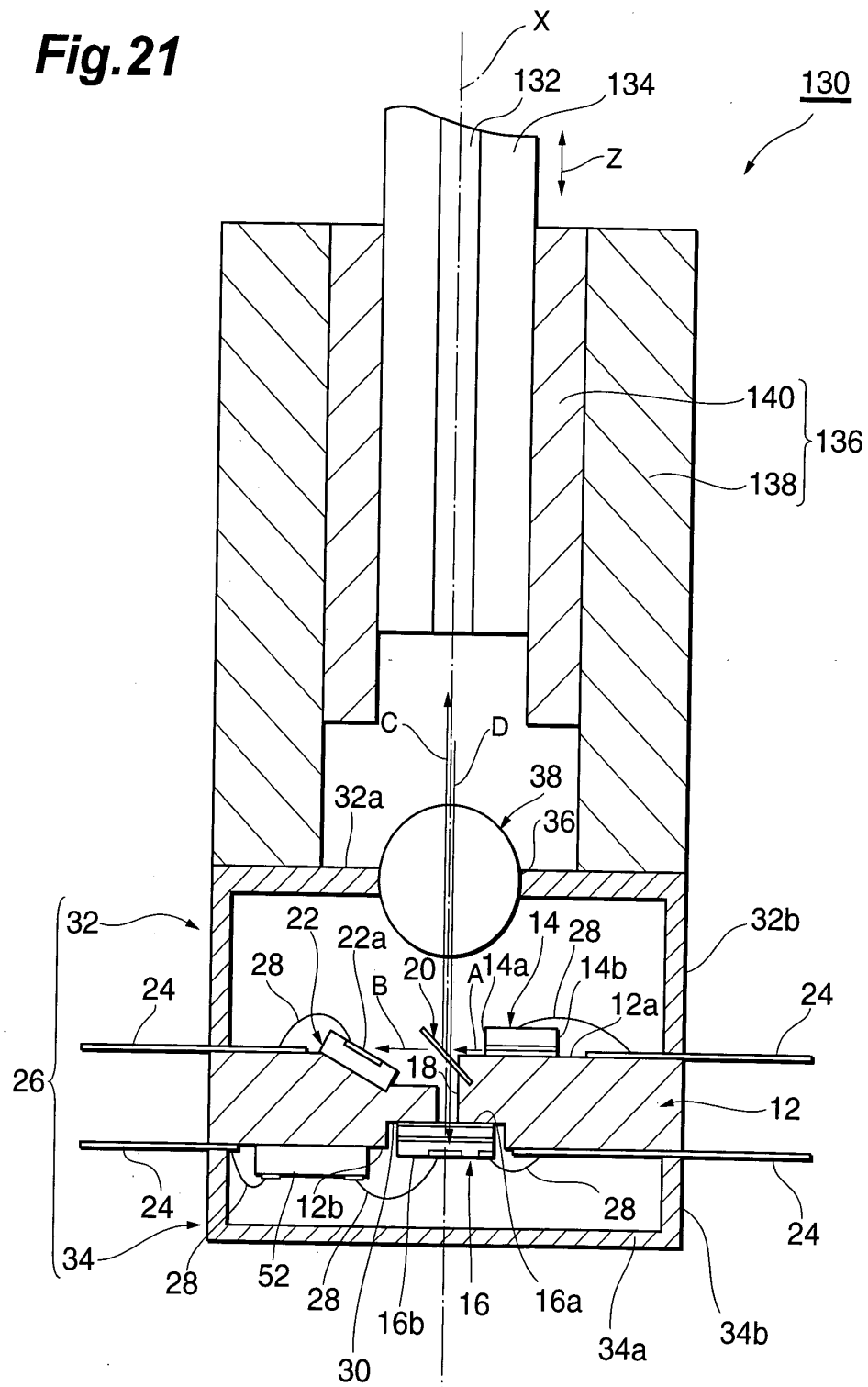


Fig.22

